UniOP Communication Drivers History

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1. A-B DeviceNet

DeviceNet communication with Rockwell Automation A-B PLCs.

UPLC94.DLL

DLL v. 5.00, DRV v. 3.04
10.04.2000

- Panel did not communicate when the DeviceNET scanner is configured with node number different from 1.

DLL v. 5.00, DRV v. 3.03
01.03.1999

- Fixed compatibility problems with A-B DeviceNet scanners of version 4 and greater.

DLL v. 5.00, DRV v. 3.02
31.03.1998

- Fixed presentation of floating point numbers

DLL v. 5.00, DRV v. 3.01
31.10.1997

- Fixed start-up problem causing an error to be reported

2. A-B DH485

DH485 network for the A- B SLC PLC's

UPLC15.DLL

DLL v. 5.00, DRV v. 3.08

08.08.1997
- Corrected internal identifier for Float special format

3. A-B DF1

UPLC10.DLL

DLL v. 5.02, DRV v. 3.08
05.12.2000
- Added support for Micrologix 1500 model

DLL v. 5.01, DRV v. 3.08
01.03.1999
- Enabled floating point support for all PLC5 models

4. AEG KS

AEG Schneider A120 PLC with KS protocol.

UPLC105.DLL

DLL v. 5.01, DRV v. 3.02
11.05.1998
- Corrected support for A120 Compact ALU201 and 202

DLL v. 5.00, DRV v. 3.01
03.02.1998
- Added support for A120 Compact

5. Altos

AL2000

UPLC62.DLL

DLL v. 5.01, DRV v. 3.06
01.03.1999
- Inserted delay before asserting RTS

6. April JBUS

Schneider Group April PLC family

UPLC102.DLL

DLL v. 5.01, DRV v. 3.09

- Minor changes to user interface

DLL v. 5.00, DRV v. 3.09
07.08.1998

7. BRU Advantage

A-B Ultra Motor drives

UPLC90.DLL

DLL v. 5.02, DRV v. 3.07
01.03.1999

DLL v. 5.00, DRV v. 3.01
14.11.1997

8. CANopen

CANopen driver.

UPLC107.DLL

DLL v. 5.04, DRV v. 3.06
17.10.2000

- Added ASCII data format support. Internal integer database contents, can now be displayed in ASCII format.

DLL v. 5.03, DRV v. 3.06
02.05.2000

- Fixed problem in version number handling. Nothing is changed in the protocol.

DLL v. 5.03, DRV v. 3.05
22.03.2000

- Fixed problem visible with bad character displayed in RDA address reference fields.
- The Address Reference field in "Define Field" dialog box is now visible.

DLL v. 5.02, DRV v. 3.05
15.03.2000

- The RDA write operations do not longer send the write event (PDO2)
- The Device SW Version readable through object 0x100A returns now the same version specified in the protocol.
- When UniOP enters preoperational mode the boot-up message is sent (it consists of a NodeGuarding message with one byte containing 0).

DLL v. 5.02, DRV v. 3.04

25.01.2000
- Fixed problem visible with panel reset when Float Data based is used.

DLL v. 5.02, DRV v. 3.03
01.03.1999

9. Compumotor 6000

Compumotor 6000 driver

UPLC47.DLL

DLL v. 5.01, DRV v. 3.05
14.05.1999
- Fixed a problem concerning Binary Variables causing communication errors while the motor is running.
- Increased timer preset.
- Mailbox did not work due to optimisatoin begin off.

DLL v. 5.01, DRV v. 3.04
05.05.1999
- Fixed bug in binary variables handling. Problem is visiable in communication errors when the motor is running.

10. Crouzet RPX

UPLC74.DLL

DLL v. 5.01, DRV v. 3.01
23.10.1997

11. CT UniDrive

Control Techniques UniDrive frequency inverters

UPLC73.DLL

DLL v. 5.01, DRV v. 3.06
20.05.1998
- Added support for UD70 and UD71.
- Complete RDA support for UD71.
- Default slave ID changed to 11.
- Changed identification string in DLL to “CT UNIDRIVE”

12. Danfoss FC

Danfoss drives FC protocol

UPLC125.DLL

DLL v. 5.03, DRV v. 3.03
02.04.1999
- Added separate SlaveIDOverride switch for Each Danfoss Node.

DLL v. 5.01, DRV v. 3.01
01.03.1999

13. Delta Tau PMAC

Delta Tau PMAC motion controllers

UPLC106.DLL

DLL v. 5.03, DRV v. 3.01
31.05.2000
- Data range extension for Turno Model

DLL v. 5.02, DRV v. 3.01
13.08.1999
- Added support for PMAC Turbo

DLL v. 5.00, DRV v. 3.00
03.02.1998

14. DeviceNet

Controller-independent generic DeviceNet

UPLC98.DLL

DLL v. 5.03, DRV. v. 3.08
11.12.2000
- Fixed problem in protocol visible under certain condition with bad data displayed by UniOP only visible whren the DeviceNET scanner is in stop mode. The bug does not affect normal functionality.

DLL v. 5.03, DRV. v. 3.07
26.07.2000
- Added Internal Work Buffer Support

DLL v. 5.02, DRV. v. 3.06

26.05.1999

- Fixed some speed problems in “non-fragmented” version.
- Fixed fragment computation problem in “fragmented” version.

DLL v. 5.02, DRV. v. 3.05
01.03.1999

- DeviceNet conformance test completed.

DLL v. 5.02, DRV. v. 3.04
14.12.1998

- Fixed some compatibility problems with A-B scanners of version 4 and greater.

DLL v. 5.02, DRV. v. 3.03
25.05.1998

- Fixed handling of fragmentation protocol in DeviceNet communication
- Checked communication with SS Tech 5136

DLL v. 5.02, DRv. v. 3.02
13.05.1998

- Added support for Inverted Word format.

DLL v. 5.01, DRV v. 3.01
27.02.1998

- Added error report if network power missing
- Several correction in the start-up phase of the network

15. Feasto EasyIP

UPLC153.DLL

DLL v. 5.03, DRV v. 3.03
28.11.2000

- First releasable version.

16. Festo FPC

UPLC23.DLL

DLL v. 5.04, DRV v. 3.11
14.03.2000

- Fixed problem in ASCII strings management by protocol

DLL v. 5.04, DRV v. 3.10
06.08.1999

- No ChangeMode to Hex for IPC/FEC. Data is exchanged in decimal.

DLL v. 5.04, DRV v. 3.09

30.04.1999
- Added support for Flags in RDA.
- Modified Defauld data in RDA handling.
- Added automatic PLC's baud rate detection
- Added baud rate changing for models IPC and FEC.
- Added support for 38400bps.

DLL v. 5.03, DRV v. 3.08
14.11.1997
- Added support for FST-FEC
- Added PLC commands to RUN and STOP programs

17. GE Series 90

GE Fanuc Series 90 PLCs. SNP and SNP-X protocols.

UPLC6.DLL

DLL v. 5.04, DRV v. 3.11
31.10.2000
- Fixed data range extension for CPUs 90-30 351/352/360/363/364

DLL v. 5.03, DRV v. 3.11
10.07.2000
- Added support for new models.

DLL v. 5.02, DRV v. 3.11
02.02.2000
- Optimized Recipe Transfer.

DLL v. 5.01, DRV v. 3.11
01.04.1998
- Fixed access to bit data types.
- Increased duration of long break in attach sequence

DLL v. 5.01, DRV v. 3.10
09.09.1997
- Added new PLC Models
- Added floating point
- Fixed error in multidrop connection

18. HC-NET

HC200 Heating Controller

UPLC71.DLL

DLL v. 5.06, DRV v. 3.07
04.05.2000
- Added support for the new model HC400.

DLL v. 5.05, DRV v. 3.06
26.02.1999
- Removed recipe download optimization for FP% data type
- Added wait state for RX activity during data request

DLL v. 5.04, DRV v. 3.06
20.01.1999

- Fixed bug in optimized recipe transfer

19. Hitachi ETH

UPLC157.DLL
07.12.2000

DLL v. 5.01, DRV v. 3.01
- First releasable version

20. Hitachi H

Hitachi H Series PLCs

UPLC24.DLL

DLL v. 5.05, DRV v. 3.15
31.10.2000
- Fixed data range extension for Micro-EH-xxxx models
- Added “Int Output WR” data type for bargraph fields.

DLL v. 5.04, DRV v. 3.15
06.10.2000
- Added support for new models: EH150 CPU308, EH150 CPU316, EH150 CPU448, Micro-EH EH-xxxx.
- Fixed bug in Office Number handling (Access to Multiple Controllers).
- Added support for optimized recipe transfer.

DLL v. 5.03, DRV v. 3.14
11.01.2000
- Fixed communication problem with EH150 model. Protocol did not manage correctly NAK messages from PLC.
- Added support for Office Number (Access to Multiple Controllers)

DLL v. 5.02, DRV v. 3.13
17.09.1999

- Fixed bad Comm ERR 01 handling in Protocol.

DLL v. 5.02, DRV v. 3.12
30.08.1999
- In case of communication errors, a NAK message is sent to PLC.
- Blocking in the ENQ sequence has been removed.
- Added 2 retries in case of communication errors.

DLL v. 5.02, DRV v. 3.10
04.05.1998
- Added new models EH150.

DLL v. 5.01, DRV v. 3.10
05.09.1997
- Added new models (H252C, etc)

21. IFM ASI-Contr.

IFM ASI Controller

UPLC64.DLL

DLL v. 5.00, DRV v. 3.00
06.10.99

22. Hitachi J

J300 series frequency inverters

UPLC124.DLL

DLL v. 5.00, DRV v. 3.00
01.03.1999

23. Interbus-S

Interbus-S driver. Compliant with MMI-COM profile.

UPLC120.DLL

DLL 5.01, DRV v. 301
- Added support for MMI-COM Word format

DLL 5.00, DRV v. 3.00
29.10.1998

24. Internal Controller

SCM03 Internal Controller

UPLC89.DLL

DLL v. 5.05, DRV v. 3.00
06.11.2000

- In “Digital I/O” data type is modified I/O point inferior limit from 1 to 0.

25. Jetter

Jetter Nano-B Process Controller

UPLC113.DLL

DLL v. 5.00, DRV v. 3.00
22.06.1998

26. K-M Sucom A

Sucom A protocol for Klockner Moeller PLCs

UPLC22.DLL

DLL v. 5.04, DRV v. 3.07
07.07.2000

- Fixed bug in protocol. The problem was visible with no communication recover in case of power cycle on the controller when the PLC had no battery installed.

DLL v. 5.04, DRV v. 3.06
11.03.1999

- Added new PLC model: PS4-341.

DLL v. 5.03, DRV v. 3.05
08.10.1997

- Protocol time-out increased to 4 seconds

27. Keyence KV

Keyence KV PLC driver

UPLC34.DLL

DLL v. 5.02, Drv. V. 3.01

07.11.2000
- IR data range extension for KV24 model

DLL v. 5.01, Drv. V. 3.01
03.10.2000
- Reversed ASCII string display

28. Koyo DL

Koyo (Direct Logic) PLCs

UPLC35.DLL

DLL v. 5.02, DRV v. 3.04
05.08.1997
- Added support for model DL105

29. Idec FA Series

UPLC19.DLL

DLL v. 5.03, DRV v. 3.07
31.05.2000
- ASCII strings were displayed in reverse.

DLL v. 5.02, DRV v. 3.07
10.06.1998
- Added support for Symbolic / Numeric Addressing.
- Multipoint addressing for FA3S CP12.

DLL v. 5.01, DRV v. 3.06
29.04.1998
- Added support for the extended data ranges for the CPU FA3S CP12 using the extended protocol commands.

30. Idec Micro 3 / ONC

UPLC42.DLL

DLL v. 5.02, DRV v.3.03
20.01.2000
- Added support for ONC Model
- Driver name changed to IDEC MICRO/ONC

DLL v. 5.01, DRV v.3.02
01.03.1999

- Added support for Micro 3C

31. Lenze CAN

UPLC140.DLL

DLL v. 5.00, DRV v. 3.01
03.11.2000
- Fixed problem in Timeout protocol handling. Protocol timeout period was wrongly calculated.

32. Matsushita FP

Matsushita FP Series PLCs

UPLC4.DLL

DLL v. 5.07, DRV v. 3.07
26.07.1999
- Added New PLC Model FP0.
- Fixed problem with RDA handling.

DLL v. 5.06, DRV v. 3.06
28.05.1999
- R Data Type was disabled in FP2 PLC model. Now it is enabled again.

DLL v. 5.05, DRV v. 3.06
01.03.1999
- Added New PLC Model FP2.
- Maximum baud rate changed to 57600 (only DLL).
- New data type added for models FP2 & FP10 (Special DT).

DLL v. 5.04, DRV v. 3.05
22.10.1998
- Added support for optimized recipe transfers

DLL v. 5.03, DRV v. 3.05
03.06.1998
- Added support for floating point

DLL v. 5.02, DRV v. 3.05
15.10.1997
- Added support for File Registers in FP3
- Added model FP0.

33. MELSEC-A

MELSEC-A protocol for Mitsubishi A Series PLCs

UPLC36.DLL

DLL v. 5.01, DRV v. 3.05
26.11.1999
- Fixed bug with bit operation

DLL v. 5.01, DRV v. 3.04
17.06.1998
- Changed message optimization parameters. Size reduced to 30 bytes for some data types.

34. Mitsubishi A

Mitsubishi A Series PLCs

UPLC18.DLL

DLL v. 5.00, DRV v. 3.05
01.03.1999
- Added auto detection sequence of the PLC CPU. This change has been introduced to support the new A1SH and A2SH CPU's.

DLL v. 5.00, DRV v. 3.04
30.09.1997
- Driver has been fixed for operation on color panels with 32 MHz CPU

35. Mitsubishi FX

Mitsubishi FX series PLCs

UPLC9.DLL

DLL v. 5.04, DRV v. 3.09
06.10.2000
- Added support for extended data range in FX2N model.
- Added support in protocol for new commands valid for FX2N model. Fixed bug visible with errors in reading operation of bits.
- Removed File Register RAM data type from FX2N model.
- Added support for Counter 32 and Counter 16 data types writing operation.

DLL v. 5.02, DRV v. 3.08
10.06.1998
- Added the model FX2N.
- Added support for floating point IEEE754 for the model FX2N.

DLL v. 5.01, DRV v. 3.08
24.02.1998

- Changed the Counter 32 bit in order to have a proper addressing of this data type. Altough it is a DWORD data type offset was calculated in WORDS resulting in wrong offsets.
- Fixed access to File Registers
- File Register data type changed to Read/Write.
- Added the model type FX0N.
- For FX model added the File Register RAM data type.

36. Modbus Slave

Modbus RTU slave protocol

UPLC127.DLL

DLL v. 5.01, DRV v. 3.02
17.09.1999

- Fixed bug in Read Coil function.
- Fixed minor bug in initialization of unused bits in Read Coil function.

DLL v. 5.01, DRV v. 3.01
01.03.1999

- Offset reference changed from 0-2047 to 1-2048.
- Increased communication speed up to 38400 baud.

37. Omron C-series

Omroc C series PLC driver

UPLC47.DLL

DLL v. 5.02 DRV v. 3.08

- Added support for new models: C200HX/HG/HE (=C200H Alpha series).
- Added support for optimized recipe transfer.

38. Philips PPCCOM

UPLC65.DLL

DLL v. 5.03, DRV v. 3.03
15.11.1999

- Added support for new model P8 CPU MMS 104

DLL v. 5.02, DRV v. 3.02
01.03.1999

- Added new model P8 CPU210

39. Profibus DP

Master-independent generic Profibus DP protocol.

UPLC97.DLL

DLL v. 5.05, DRV v. 3.04
04.08.1999
- Mimimum slave number set to 1.
- Maximum slave number set to 125
- Default slave number is 1 (it was 2 in the previous version).
- Slave number 126 has been removed because nornally assigned to slaves that can change the address.
- PNO certified.

DLL v. 5.04, DRV v. 3.03
01.03.1999
- Fixed bug in Read Configuration message.
- Fixed RDA data types selection list and default addresses.

DLL v. 5.03, DRV v. 3.02
02.06.1998
- Changed PLC operator strings: E replaced by I, A replaced by Q.

DLL v. 5.02, DRV v. 3.01
04.05.1998
- Added support for inverted byte ordering.
- Corrected bit write commands

40. Profibus DP S5

UPLC87.DLL

DLL v. 5.02, DRV v. 3.03
16.07.1999
- Modified soft state machine to sync with SPC3 state machine.
- PM interface upgrade.
- Error codes revised.

DLL v. 5.02, DRV v. 3.02
01.03.1999
- Optimized message length set to 20 bytes.
- Fixed bug in Read Configuration message.

DLL v. 5.01, DRV v. 3.01

23.07.1998
- Added support for optimized recipe transfer

DLL v. 5.00, DRV v.3.01
03.09.1997
- Added support for the EXOR Manufacturer Code. Important note: the Profibus DP configuration on the master MUST be changed to the new EX9649AX.GSD.

41. Profibus DP S7

UPLC75.DLL

DLL v. 5.03, DRV v. 3.04
16.07.1999
- Modified soft state machine to sync with SPC3 state machine.
- PM interface upgrade.
- Error codes revised.

DLL v. 5.03, DRV v. 3.03
01.03.1999
- Fixed DB adressing mode visible when in Designer you attempt to use big DB numbers.

DLL v. 5.02, DRV v. 3.03
15.01.1999
- Optimized message length set to 20 bytes
- Fixed bug in Read Configuration message.

DLL v. 5.01, DRV v. 3.02
23.07.1998
- Added support for optimized recipe transfer

DLL v. 5.00, DRV v. 3.01
04.09.1997
- Added support for the EXOR Manufacturer Code. Important note: the Profibus DP configuration on the master MUST be changed to the new EX9649AX.GSD.
- Fixed recovery from low level line errors

42. Profibus DP TI

Profibus DP for the Simatic TI 555.

UPLC119.DLL

DLL v. 5.00, DRV v. 3.01
01.03.1999
- Fixed bug in Read Configuration message.

DLL v. 5.00, DRV v. 3.00

11.09.1998

43. SAIA S-BUS

UPLC83.DLL

DLL v. 5.03, DRV v. 3.08
12.06.2000
- Fixed bug in Flag, Input and Output operation. It was introduced in the previous version.

DLL v. 5.03, DRV v. 3.07
07.03.2000
- Added SAIA RTC data type
- Added Data Block data type
- Implemented checksum validation

DLL v. 5.02, DRV v. 3.06
24.09.1999
- Fixed problem visible in I/O access.

DLL v. 5.02, DRV v. 3.05
10.09.1999
- Fixed wrong ASCII handling in DLL.

DLL v. 5.01, DRV v. 3.05
19.07.1999
- Fixed problem in RDA mapping.

DLL v. 5.00, DRV v. 3.04
18.06.1999
- - Fixed problem with Toggle Bit Macro when Alarms are configured in the project.
- - Floating Point did not work at all!
- - Fixed COMM ERR 05 on FW38 panels.

DLL v. 5.00, DRV v. 3.01
01.03.1999
- This version allows multiple double word writes. Fixed problem visible in the RDA keys area where only the first double word of keys was working.

DLL v. 5.00, DRV v. 3.00
21.11.1997

44. SattCon COMLI

UPLC46.DLL

DLL v. 5.02, DRV v. 3.01

13.05.1998

- For SattCon 05 changed the max I/O offset from 3071 to 5777 (octal).
- For SattCon 35 changed the max I/O offset from 2777 to 7777 (octal). Also, the new data type is added for this model - Extended I/O 12000-23777).
- Added Extended I/O to Direct Access

45. SDS

Honeywell Smart Distributed System

UPLC112.DLL

DLL v. 5.04, DRV v. 3.03
30.08.2000

- Fixed problem in protocol interrupt routine handling. Some interrupts were lost causing communication errors.

46. SEW Eurodrive

Communication with Movidyn and Movitrac controllers

UPLC93.DLL

DLL v. 5.01, DRV v. 3.01
15.05.1998

- Enabled R/W on 'Standard' parameters
- Changed communication retry timeout from 5000 ms to 1000 ms.
- Fixed steps for Movidyn's parameter 717 (Positioning Ramp)

47. SIEI S-LINK 1

S-LINK 1 protocol for SIEI Drives

UPLC143.DLL

DLL v. 5.00, DRV v. 3.00
11.01.2000

48. SEW Movilink

Movilink protocol for Movidrive from SEW Eurodrive

UPLC 118.DLL

DLL v. 5.02, DRV v. 3.02
26.10.1998

49. Siemens 3964R

Siemens 3964R protocol with RK512 command interpreter

UPLC14.DLL

DLL v. 5.02, DRV v. 3.08
23.07.1998
- Added support for optimized recipe transfer

DLL v. 5.01, DRV v. 3.08
30.03.1998
- Removed Timer and Counter status data types.
- Fixed bit access
- Disabled write operations for all data types except Data Block.

50. Simatic S5

AS511 protocol for Siemens Simatic S5

UPLC2.DLL

DLL v. 5.01, DRV v. 3.08
23.07.1998
- Added support for optimized recipe transfer

51. Simatic S7 MPI

UPLC55.DLL

DLL v. 5.04, DRV v. 3.06
03.07.2000
- Modified startup sequence to match the Siemens OP behavior during network init. This should allow use of MAX number of UniOP panels in MPI networks without using PLC resources dedicated to programmer.

DLL v. 5.04, DRV v. 3.05
02.02.2000
- Added support for Signed/Unsigned Data Format.

DLL v. 5.03, DRV v. 3.05
01.03.1999
- Fixed DB addressing mode visible in Designer when you attempt to use big DB numbers.

DLL v. 5.02, DRV v. 3.05
16.10.1998
- Fixed start-up problem. The problem manifested as multiple communication errors while starting up the communication (in some cases also several seconds).

DLL v. 5.02, DRV v. 3.04
23.07.1998
- Added support for optimized recipe transfer

52. Simatic S7 PPI

PPI protocol for Simatic S7-200

UPLC54.DLL

DLL v. 5.07, DRV v. 3.05
13.10.2000
- Fixed small problem in RDA handling. Normally it is not visible at user level.

DLL v. 5.06, DRV v. 3.05
28.09.2000
- Fixed problem visible using 22x CPU models. Protocol was not working correctly when the 22x models were selected.

DLL v. 5.05, DRV v. 3.05
07.06.2000
- Added support for new models (CPU 22x)

DLL v. 5.04, DRV v. 3.05
13.08.1999
- Added support for PPI+ protocol

DLL v. 5.02, DRV v. 3.03
01.03.1999
- Fixed bug with checksum calculation in some special cases.

DLL v. 5.02, DRV v. 3.02
23.07.1998
- Added support for optimized recipe transfer

DLL v. 5.01, DRV v. 3.02
17.10.1997

- Supports CPU 215/216

53. Galil/Yaskawa

Yaskawa – SMC200 Motion Controller

UPLC82.DLL

DLL v. 5.01, DRV v. 3.04
17.05.1999
- Fixed a problem with with limited number of characters in macro commands.

54. Square D SY/MAX

UPLC11.DLL

DLL v. 5.00, DRV v. 3.06
30.03.1999
- Fixed Bug with ID check

55. Suconet K

Klockner Moeller Suconet K network

UPLC101.DLL

DLL v. 5.05, DRV v. 3.11
21.12.1999
- Fixed bug visible in communication broken after PLC is left in STOP for long time

DLL v. 5.05, DRV v. 3.10
01.12.1999
- The bug fix introduced in Drv V3.09 had a side effect on background communication. Problem is visible with very bad performances in case background activity is programmed in the Designer project.

DLL v. 5.05, DRV v. 3.09
04.08.1999
- Fixed bug with protocol cache handling in case of non cyclical data
- Fixed problem visible in toggle bit macro when multiple panels are connected to the same Suconet K master.

DLL v. 5.05, DRV v. 3.08
25.05.1999
- Fixed bug visible in case of communication problems when the panel could stop to refresh variables. The problem showed up with RDA programmed (the write operation was the point where the protocol get blocked).

DLL v. 5.05, DRV v. 3.07

01.03.1999
- Fixed bug with protocol cache handling. Problem was visible during alarms printout with no variables on the page.
- Added new PLC model PS4-341.

DLL v. 5.04, DRV v. 3.06
- Changed message optimization length
- Added read cache for higher performance
- Updated Suconet module. Includes the K-M SIS_EXT.OBJ (Firstly done in version 3.05 hich has never been released).

56. Toshiba

UPLC37.DLL

DLL v. 5.00, DRV v. 3.01
28.09.1999
- Fixed bug visible in reading and writing operation starting from odd offset or using an odd number of bytes. The problem was visible in wrong bit read/write operations.

57. Toshiba T

UPLC43.DLL

DLL v. 5.03, DRV v. 3.02
22.08.2000
- Addes support for IEEE standard floating point data format

58. Toshiba T CL

Computer Link protocol for Toshiba T Series

UPLC86.DLL

DLL v. 5.04, DRV v. 3.00
22.08.2000
- Added support for IEEE standard floating point data format

DLL v. 5.03, DRV v. 3.00
01.03.1999
- Reduced Number of Bytes For Optimization from 58 to 32

DLL v. 5.02, DRV v. 3.00
22.10.1997

59. UNITELWAY

Schneider Group Unitelway protocol

UPLC25.DLL

DLL v. 5.02, DRV v. 3.11
13.05.1998
- Increased the max offset of %M data type, from 255 to 3962 for the model TSX57 Premium.

DLL v. 5.01, DRV v. 3.10
17.10.1997
- Added model TSX57 Premium.

60. VT100

VT100 Emulation driver

UPLC27.DLL

DLL v. 500, DRV v. 4.01
20.11.1997
- added support for operation on VGA displays
- added default communication parameters setting from project file
- added support for operation at 38400 baud
- added support for SPACE parity

DLL v. 500, DRV v. 4.00
07.08.1997
- Changed protocol internals